|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **ECO TITLE** | | | | | |
| VIVA Board Revision | | | | | |
| **ECO NO\*** | 1024 | **REQUESTED BY** | N. MANTEUFEL | **DATE** | 06Mar2024 |
| **DWG NO** | 11A-001 | **REVIEWED BY** | Click or tap here to enter text. | **DATE** | Click or tap to enter a date. |
| **DWG REV (NEW)** | A2 | **APPROVED BY** | Click or tap here to enter text. | **DATE** | Click or tap to enter a date. |
|  | | | | | |
| **ORDER** | | | | | |
| **DESCRIPTION OF CHANGE** | | | | | |
| Remove AREF from ground. Add 1uF capacitor to AREF signal. | | | | | |
| **STATEMENT OF RISK** | | | | | |
| No known risk. | | | | | |
| **JUSTIFICATION OF CHANGE** | | | | | |
| Correct ADC issue with Atmega328P. | | | | | |

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|  |
| Added C24 |

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| --- | --- | --- | --- | --- |
| **Form Controls** | | | | |
|  | |  |  |  |
| Log Entry Key | 20240306185145 | | | |
| Computer ID |  | | | |
| User ID |  | | | |
| Date Time |  | | | |
| PDF |  | | | |